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~~CLOCK SWITCHING CIRCUIT~~ PLEASE SCAN THE ABSTRACT
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ABSTRACT OF THE DISCLOSURE

A clock switching circuit is provided for switching from a first clock signal being output to a freely selected second clock signal among a plurality of clock signals having different frequencies and phases while preventing generation of a hazard, comprising. The switching circuit has a plurality of unit circuits for respectively receiving as an input the clock signals, selection signals of the clock signals and enabling signals and controlling controls supplying and stopping of the clock signals in accordance with the selection signals and the enabling signals, and a. A feedback circuit for monitoring monitors output conditions of the plurality of unit circuits and, when outputting of all clock signals of the plurality of unit circuits was stopped as a result of stopping the first clock signal, giving a plurality of the unit circuits the enabling signals for approving starting of a supply of the second clock signal.